# ★ C\*Core Technology Co. 务州国芯科技育限公司

# **RISC Core C312 Data Sheet**

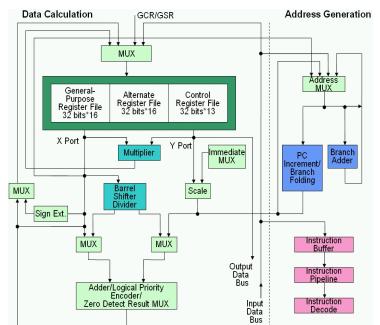
#### **Summary**

The C312 is a member of the C\*Core<sup>TM</sup> High-Performance Low-Power 32-bit RISC core family. It has been designed for high-performance and cost- sensitive embedded applications, with particular emphasis on reduced system power consumption, making it suitable for battery-operated, portable products. The C312 is 100% compatible with the C310 core, which extends the C210 instruction set and functionality by integrating an integer multiplier, branch prediction and folding, improved pipelining of load and store operations, and a deeper instruction buffer to improve memory bandwidth utilization. The C312 can run 33% faster than the C310. The C312 hard macro is available in two versions, one optimized for high performance and one optimized for size and low power.

#### **Core Features**

- Low power RISC core
- ➢ 32-bit load/store architecture
- ➢ Highly optimized pipeline
- ➢ Single-cycle 32x16 multiplier
- Fixed-length 16-bit instructions
  - † Mostly single-cycle execution† Two-cycle branch execution
- ▶ 16 32-bit general purpose registers
- ➤ 13 32-bit control registers

## **Performance and Characteristics**



- C\*Bus MLB bus architecture
  - † Support byte/halfword/word access
  - † Optional AMBA wrapper
- Fast interrupt support
  - † 16 32-bit alternate registers for fast context switching
  - † Vectored/auto-vectored interrupts
  - † 128 interrupt/exception vectors
- ➤ Debug support via JTAG-based OnCE<sup>TM</sup> Design

| <b>Process</b><br>HHNEC25 Speed Optimized | Frequency (wcs)<br>90 MHz | MIPS (wcs)<br>86 | Size<br>TBD mm <sup>2</sup> | <b>Power</b> (typ)<br>TBD mW/MHz | <b>Leakage</b> (typ)<br>TBD µW |
|---|---------------------------|------------------|-----------------------------|----------------------------------|--------------------------------|
| HHNEC25 Area/Power Optimized              | 60 MHz                    | 57               | $TBD mm^2$                  | TBD mW/MHz                       | TBD μW                         |
| SMIC18 Speed Optimized                    | 133 MHz                   | 126              | $0.628 \text{ mm}^2$        | 0.50 mW/MHz                      | 30.5 μW                        |
| SMIC18 Area/Power Optimized               | 80 MHz                    | 76               | $0.604 \text{ mm}^2$        | TBD mW/MHz                       | $TBD\mu W$                     |

## **Application Examples**

- Smart Cards
- Consumer Electronics

#### Availability

≻ Q2, 2007

Office Automation

Communication/Network

To obtain more information about the C312 or other  $C^*CORE^{TM}$  products, please contact the  $C^*Core$ Technology Co., Ltd. by phone: 0512-68091372, email: support@china-core.com or web: http://www.china-core.com.

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