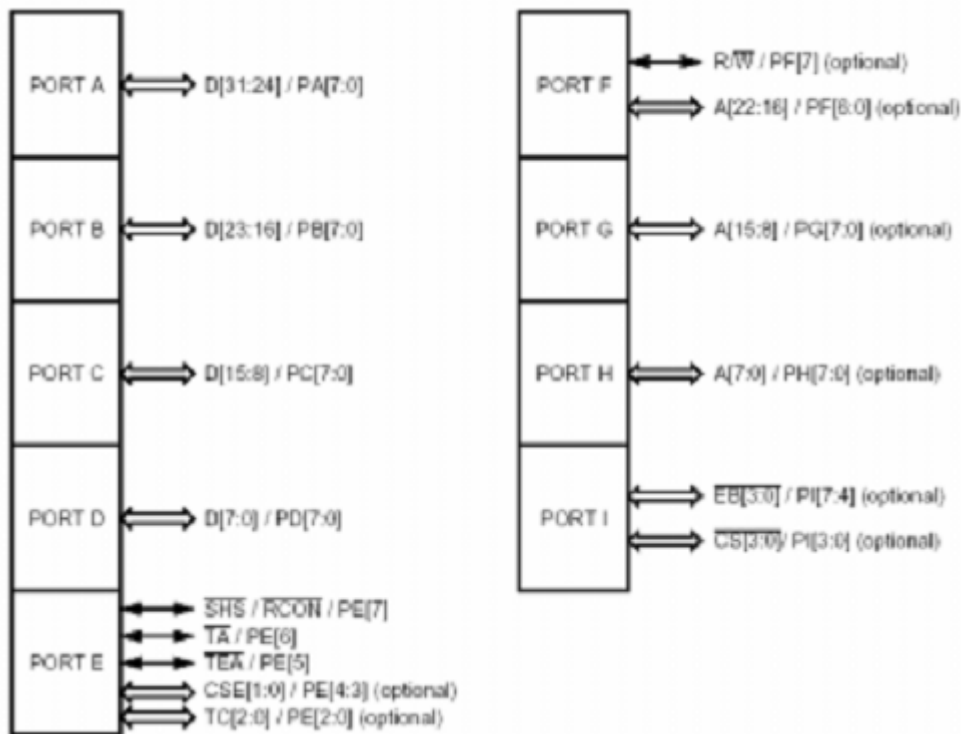


PORTS Module

Summary

Many of the pins associated with the external interface may be used for several different functions. Their primary function is to provide an external memory interface to access off-chip resources. When not used for their primary function, many of the pins may be used as general-purpose digital I/O pins. In some cases, the pin function is set by the operating mode, and the alternate pin functions are not supported.

Digital I/O pins are grouped into 8-bit ports. Each port has registers that configure, monitor, and control the port pins.



PORTS Block Diagram

Features

- Ability for software to configure pins for several different functions
- When not use primary function, pins can used as GPIO
- Digital I/O pins grouped into 8-bit ports

To obtain more information about the PORTS or other C*Core™ products, please contact the C*Core Technology Co., Ltd. by phone: 0512-68091377, email: support@china-core.com or web: <http://www.china-core.com>.

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